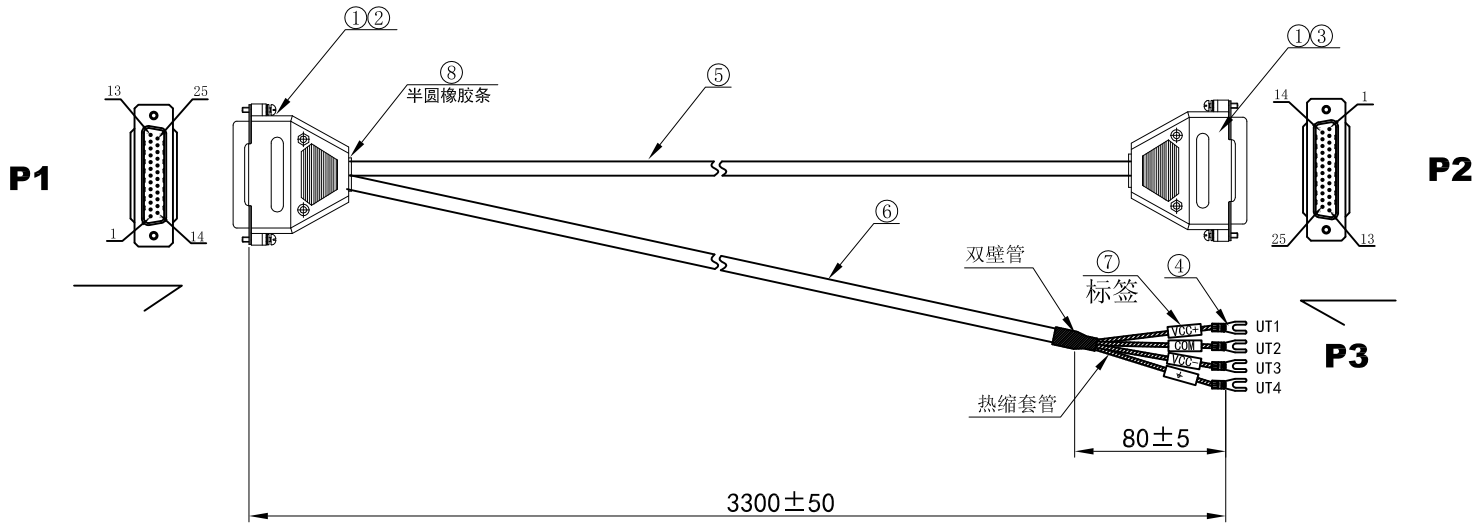
图号：	303-03526

连接定义：

P1	P2	P3	线色	标签
1	1		红	CLK-
14	14		红白点	CLK+
2	2		橙	SYNC-
15	15		橙黑点	SYNC+
3	3		黄	X-
16	16		黄黑点	X+
4	4		绿	Y-
17	17		绿黑点	Y+
5	5		蓝	Z-
18	18		蓝白点	Z+
6	6		紫	STATUS1-
19	19		紫白点	STATUS1+
7	7		黑	STATUS2-
20	20		黑白点	STATUS2+
8	8		棕	STATUS3-
21	21		棕白点	STATUS3+
外壳	外壳		屏蔽	
9		UT1	红	VCC+
22			白红	
10			棕	
23		UT2	蓝	COM
11			白蓝	
24			绿	
12		UT3	黑	VCC-
25			白黑	
13			白绿	
外壳		UT4	屏蔽	⏚



	签名	日期	型号： DB25接口3.3米	版面：	A4
绘图：				单位：	mm
校对：			品名： 电源线	页码：	1/1
核准：					
	精准光学				

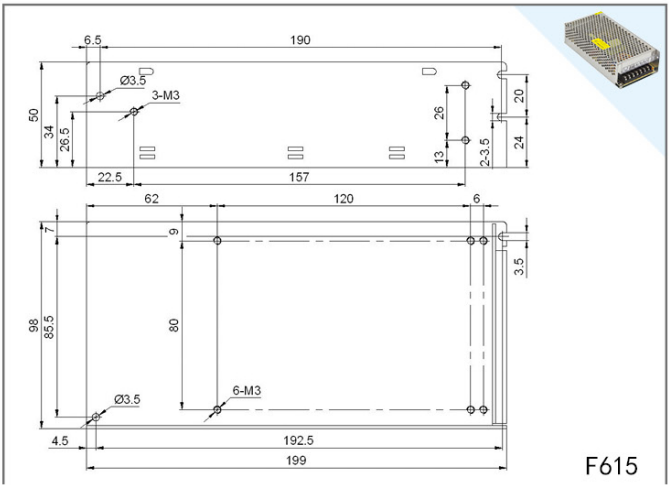
150W Dual Output Standard Series

- High reliability, low cost
- Top brand components for key parts
- Built-in EMI filter
- Protections: overload/ short circuit
- 5 years limited warranty
- F615 199 × 98 × 50(mm)

AC input voltage range	170~264VAC (210~370VDC)
Inrush current	cold start, 40A/230V
Input leakage current	＜0.7mA/ 230VAC
Line regulation (full load)	≤±0.5%
Voltage tolerance	V_1: ≤±1%, V_2: ≤±3% (with regulator); ≤±10% (without)
Output voltage adjust range	V_1: ±5% of rated output voltage
Output overload protection	105~130%, shut off, re-power on to recover
Withstand voltage	I/P -O/P: 1.5KVAC/1min I/P -F/G: 1.5KVAC/1min O/P-F/G: 0.5KVAC/1min
Rise time	50ms@full load (typical)
Hold up time	20ms@full load (typical)
Operating temperature	-20℃~+50℃
Safety standards	design meet GB4943, UL60950, EN60950
Cooling method	convection

Model	DC Output		R&N	Efficiency
HF150W-D-L	15V	0.5~5.0A	120mV	85%
	-15V	0.5~5.0A	120mV	

Drawing



XY2-100 technical datasheet

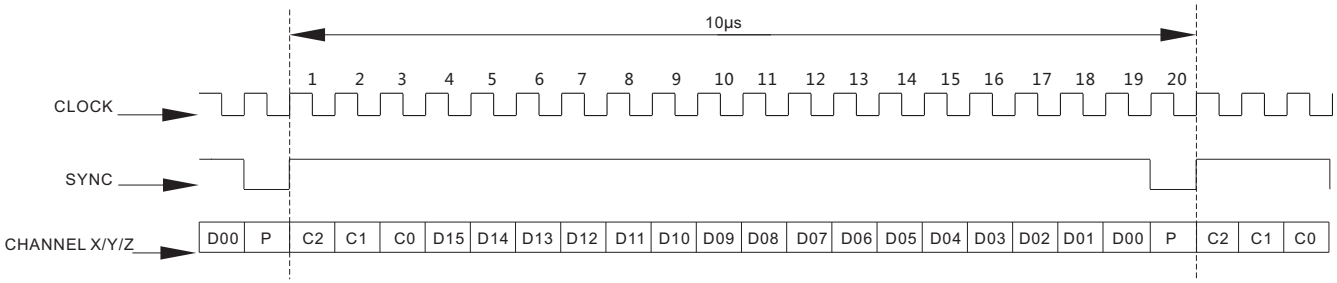
General description

The XY2-100 interface is used to send X and Y coordinates from the controller to the deflection system.It is a serial interface using 16-bit words,sent with a speed of 2 Mbit/s .This document describes the IO-pin configuration,the signal description and the timing specifications.

Pin configuration (with Ray-motion DB25 connector)

Pin	Name	Signal description	In/Out
1/14	CLOCK-/CLOCK+	CLOCK :Continuously running clock	Input
2/15	SYNC-/SYNC+	SYNC :Synchronises data transfer	Input
3/16	CHAN1-/CHAN1+	CHANNEL X :Data to X axis	Input
4/17	CHAN2-/CHAN2+	CHANNEL Y :Data to Y axis	Input
5/18	CHAN3-/CHAN3+	CHANNEL Z :Data to Z axis	Input
6/19			
7/20			
8/21			
9/10/22	POWER+	+15V	Input
11/23/24	GND		Input
12/13/25	POWER-	-15V	Input

Signal description



DATA(CHANNEL X,CHANNEL Y,CHANNEL Z)

The data of each axis consist of 20-bit words.The first 3 bits are used as a control word(C2-C0).The next 16 bits are data information(D15-D0, offset binary) and the last bit is a parity bit(P, even parity).

SYNC

The transfer of data is synchronised using a synchronisation signal.The SYNC bit goes high when the first bit can be sent. It remains high for 19 bits and goes low when the parity can be sent.

CLOCK

The clock signal runs at a frequency of 2 MHz.When it goes high,the data bit changes.When it goes low,the data bit is sampled by the deflection system.

Timing specifications

Description	Name	Min	Typ	Max	Units
data-in setup time	tDS	50			ns
data-in hold time	tDH	100			ns

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